į ė

M-9631 US KEVIN D. MORISHIGE

HARDWARE LOAD BALANCING THROUGH A

SINGLE FABRIC

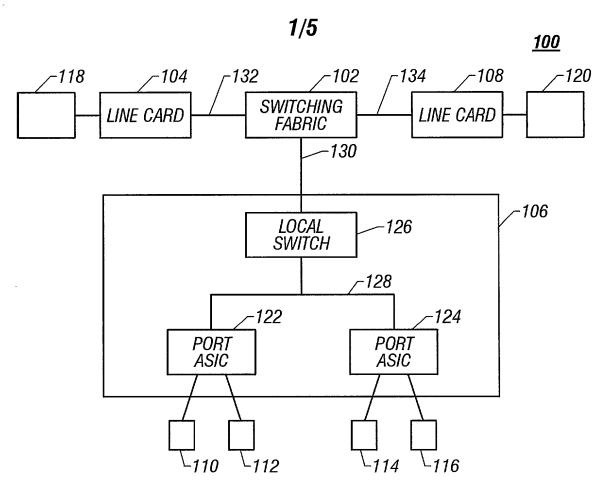


FIG. 1

<u> 200</u>

SOURCE IP ADDRESS	<u>202</u>
SOURCE PORT NUMBER	<u>204</u>
DESTINATION IP ADDRESS	<u>206</u>
DESTINATION PORT NUMBER	<u>208</u>
DATA	<u>210</u>
DATA	<u>210</u>
•	
DATA	<u>210</u>

FIG. 2

Attorney Docket No.: First Named Inventor:

Title:

M-9631 US

KEVIN D. MORISHIGE HARDWARE LOAD BALANCING THROUGH A

SINGLE FABRIC

2/5

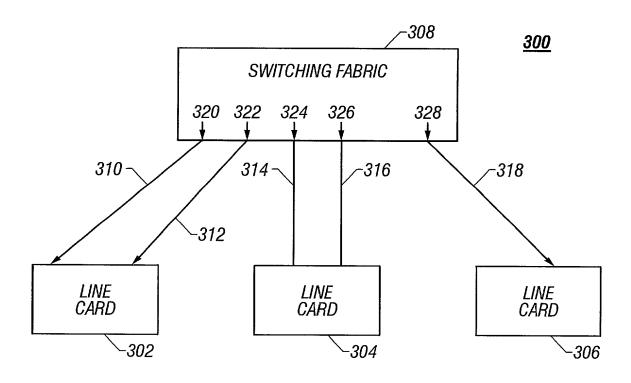


FIG. 3

that the first all all all the first that that

<u>k</u> al

for the the start with

Attorney Docket No.: First Named Inventor: Title:

4.1.1 A.1.1

ž

M-9631 US

KEVIN D. MORISHIGE HARDWARE LOAD BALANCING THROUGH A

SINGLE FABRIC

3/5

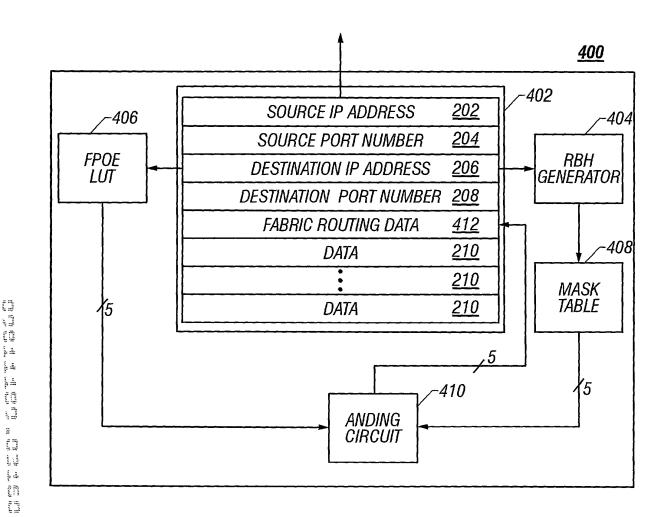


FIG. 4

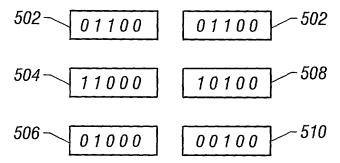


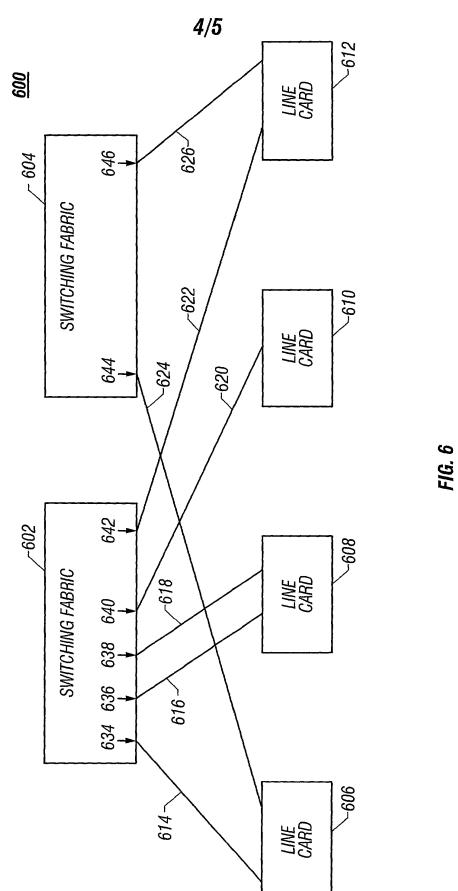
FIG. 5

Attorney Docket No.: First Named Inventor:

Title:

M-9631 US KEVIN D. MORISHIGE HARDWARE LOAD BALANCING THROUGH A

SINGLE FABRIC



l d

4.1

13

Attorney Docket No.: First Named Inventor: Title:

M-9631 US KEVIN D. MORISHIGE

HARDWARE LOAD BALANCING THROUGH A

SINGLE FABRIC

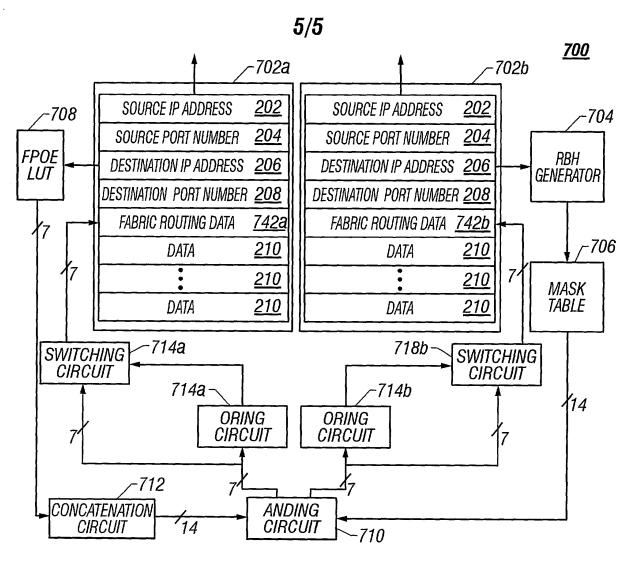


FIG. 7

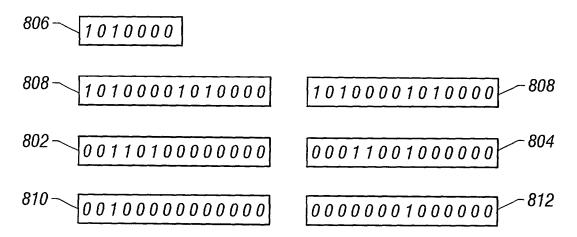


FIG. 8